

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. ....10/050,348  
Filing Date ..... January 15, 2002  
Inventor ..... Gurtej S. Sandhu  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Laura M. Schillinger  
Attorney's Docket No. .... MI22-1898  
Title: Transistor Structures

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

**References – See Attached Form PTO/SB/08a**

The attached form PTO/SB/08a is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1273 Off. Gaz. Pat. Off. 55, 05 August 2003). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: May 4, 2006

By: Jennifer J. Taylor

Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711